

<b>Notice of References Cited</b>	Application/Control No. 10/501,903		Applicant(s)/Patent Under Reexamination VORBACH ET AL.	
	Examiner LOREN CHAUHAN		Art Unit 2193	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-7,249,351 B1	07-2007	Songer et al.	717/158
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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	N					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Automatic Allocation of Arrays to Memories in FPGA Processors With Multiple Memory Banks by Maya Gokhale, Janice Stone Field-Programmable Custom Computing Machines, Seventh Annual IEEE Symposium Proceedings on 21-23 April, 1999 pages 63-69
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.